

FIG. 1

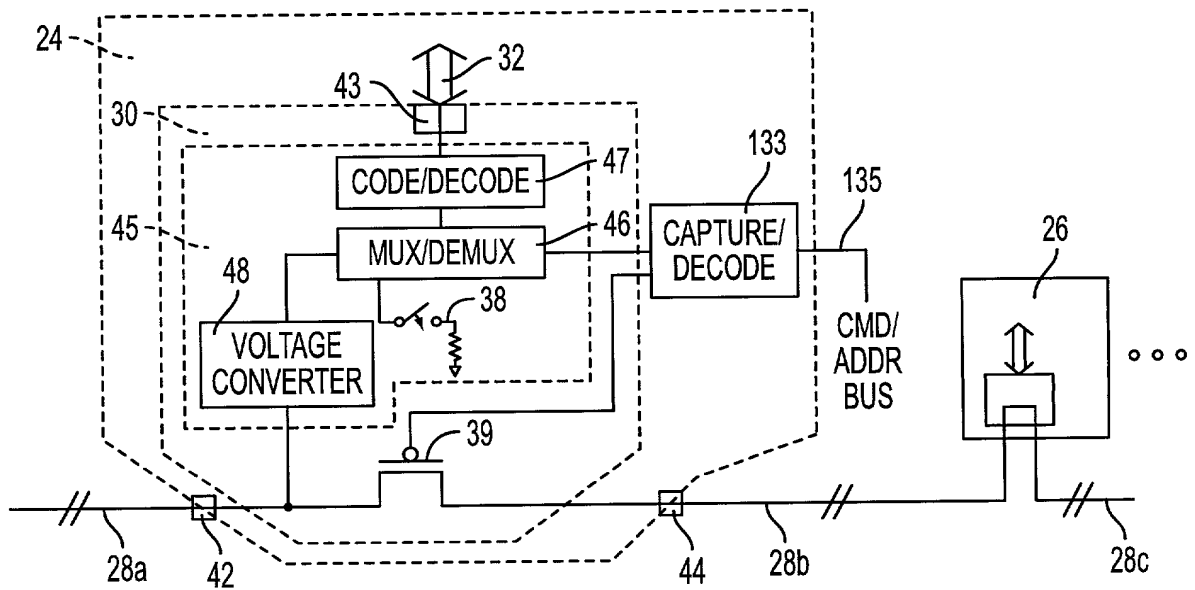


FIG. 2

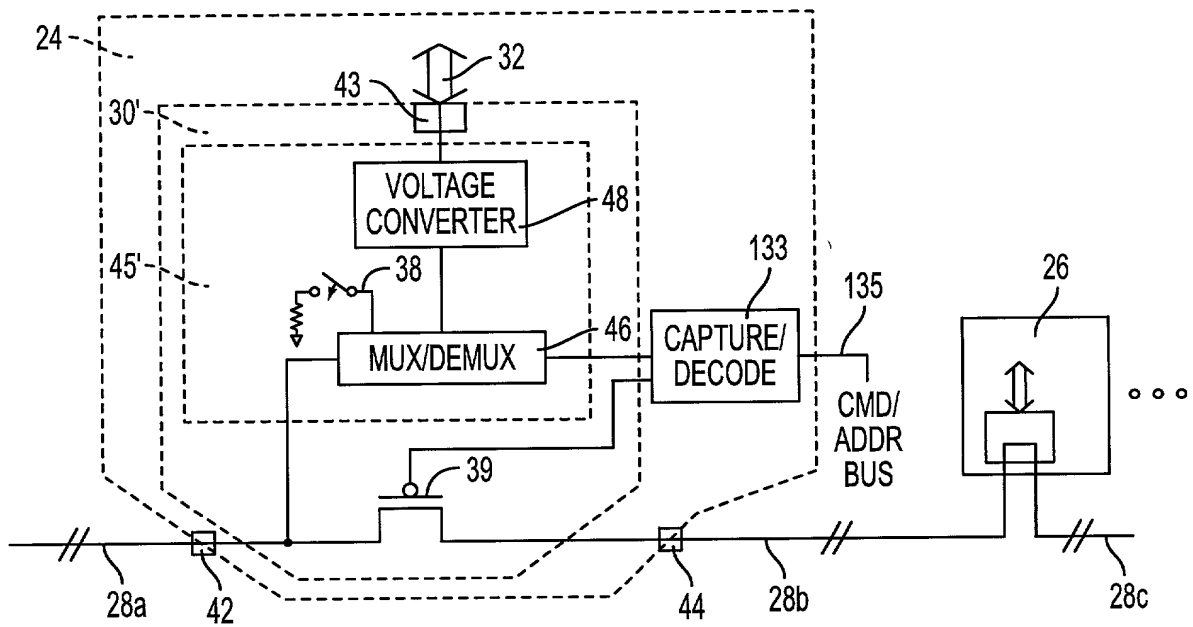


FIG. 3

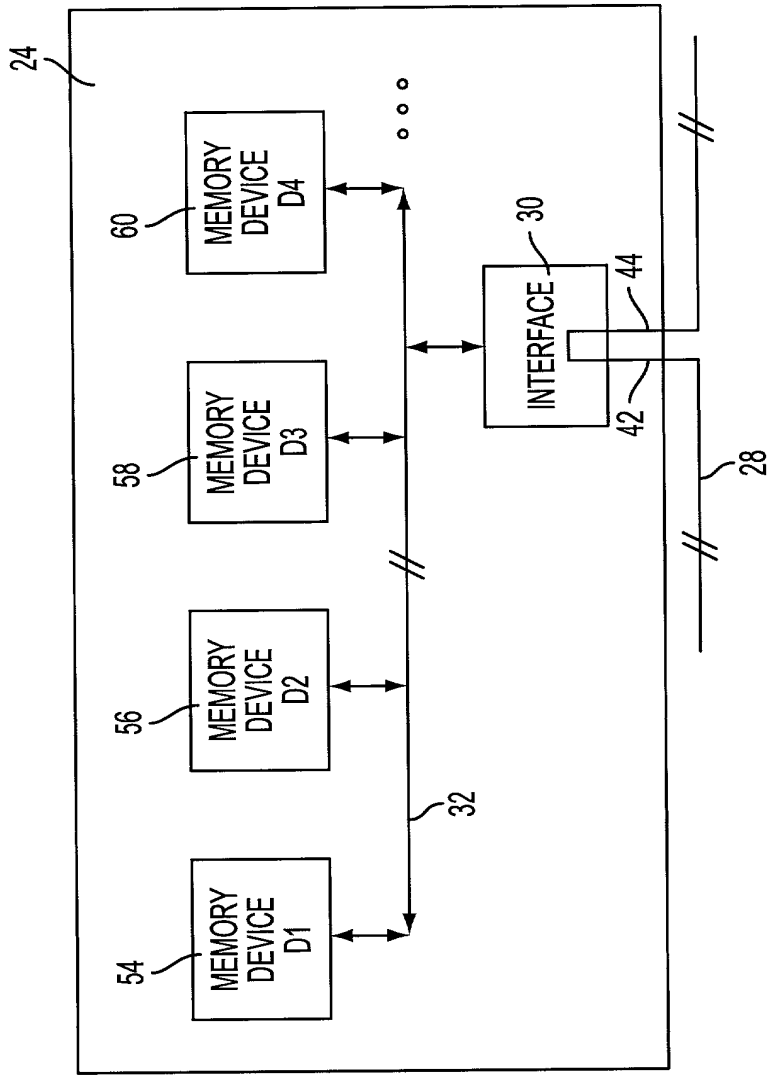


FIG. 4

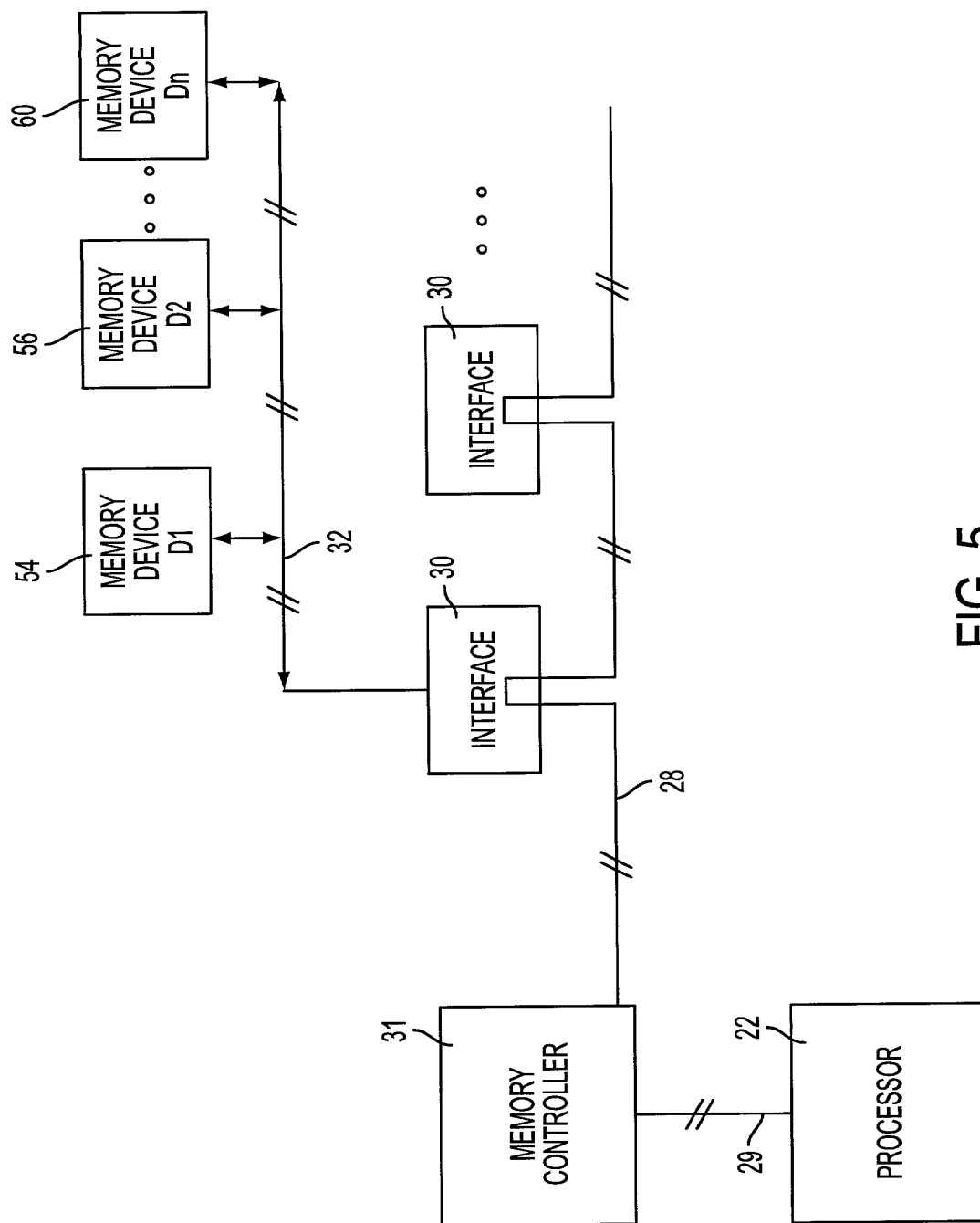


FIG. 5

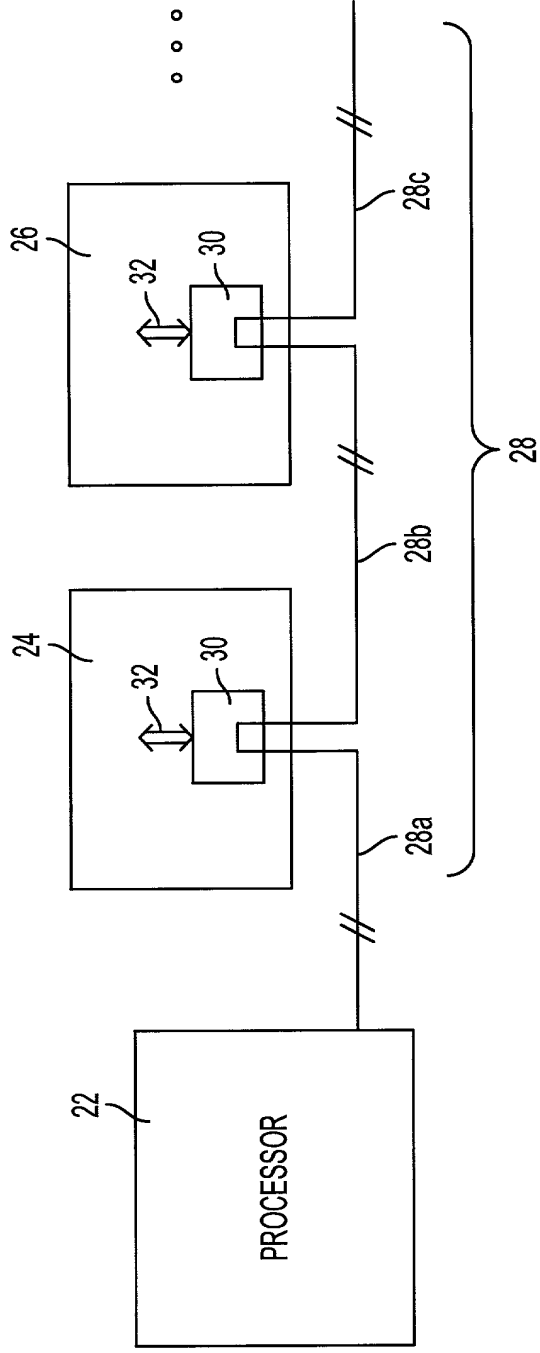


FIG. 6

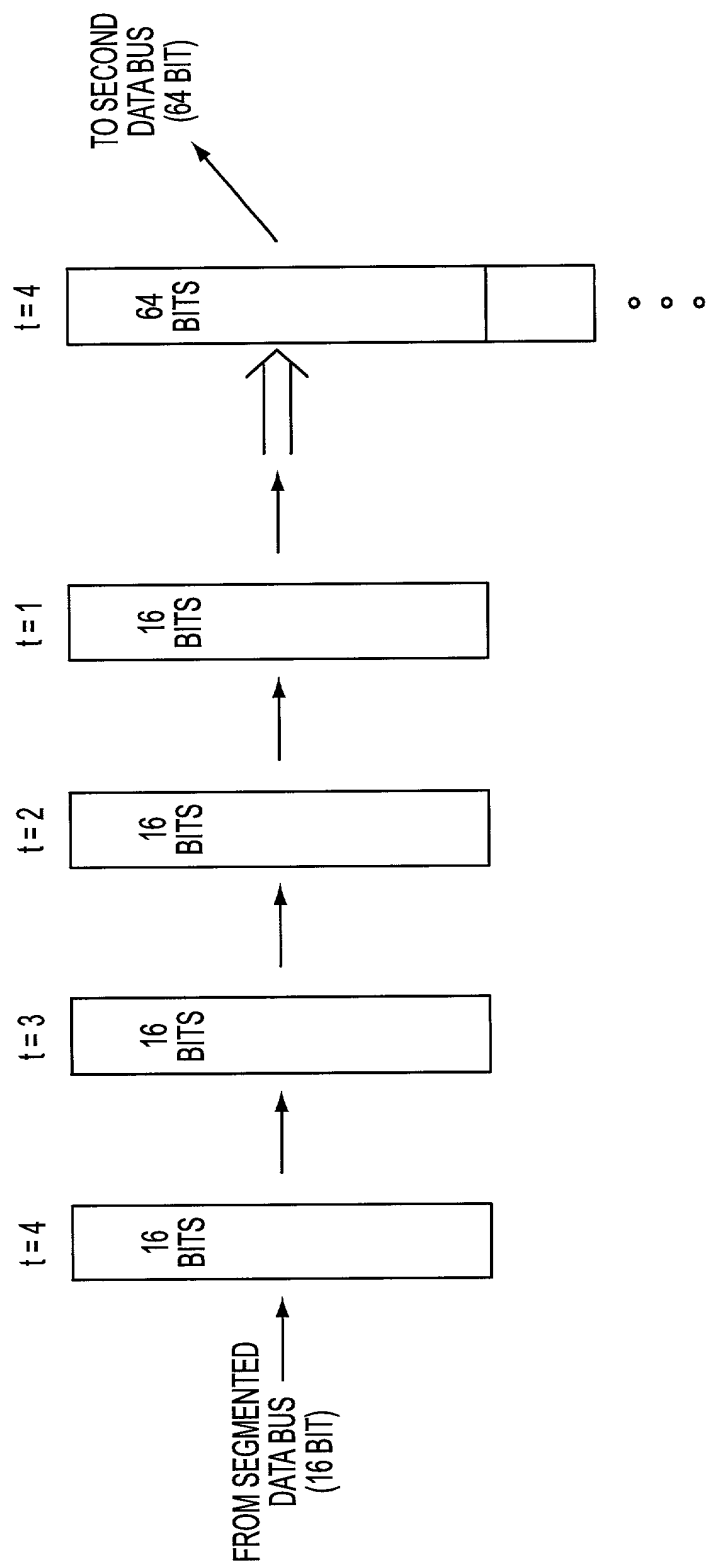


FIG. 7

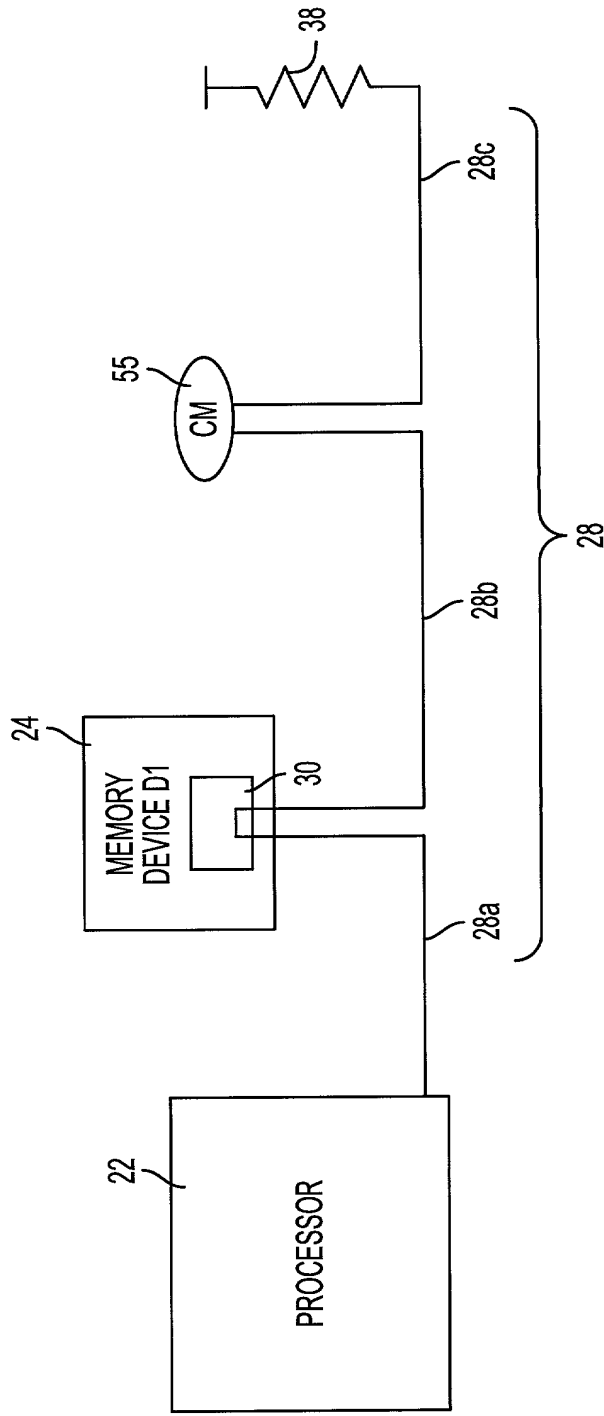


FIG. 8

FIG. 9 is a schematic diagram of a device 24, which includes a central region 30 and a surrounding region 32. The device 24 is shown with a series of lines 28a and 28b extending from the central region 30 to the surrounding region 32.

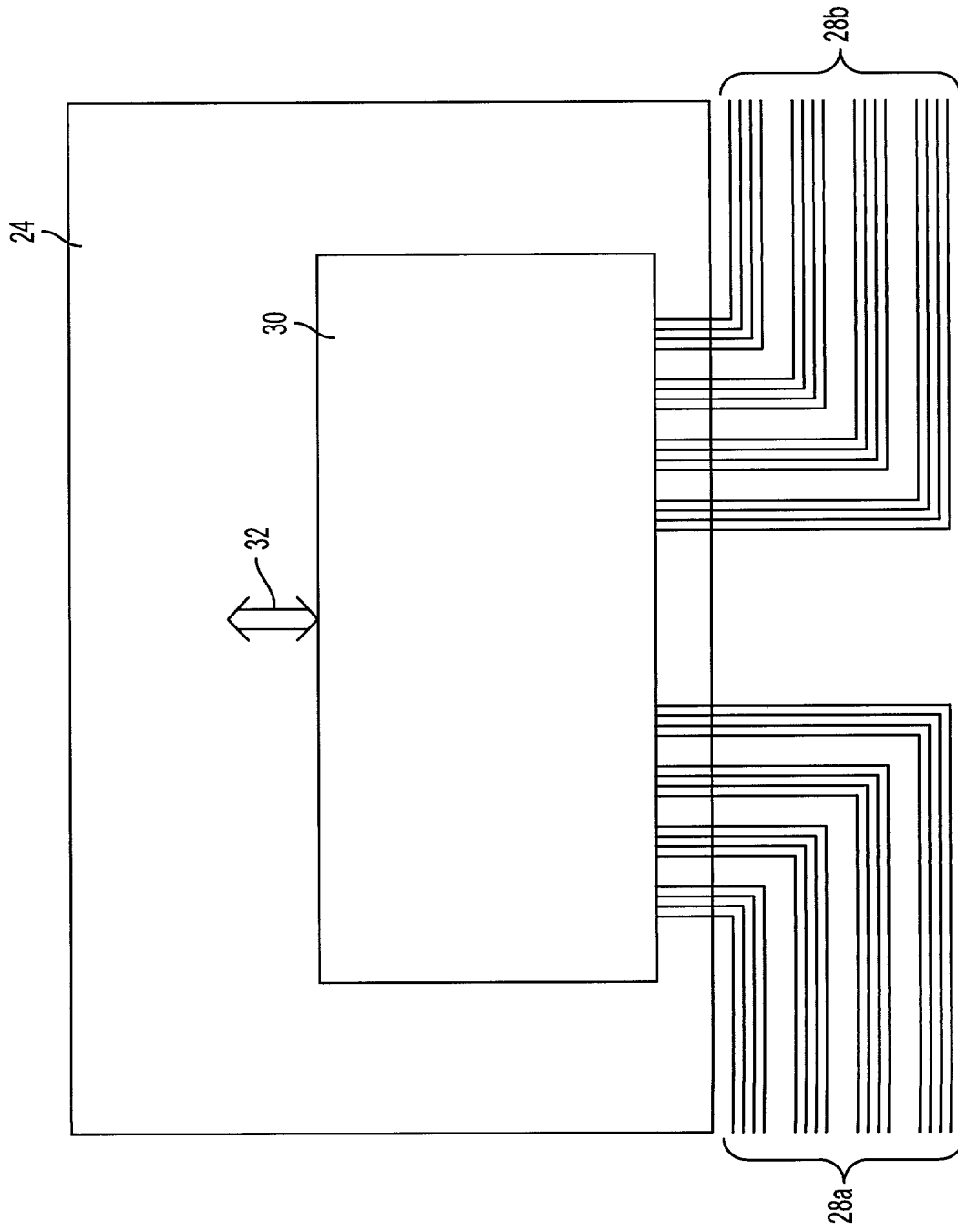


FIG. 9



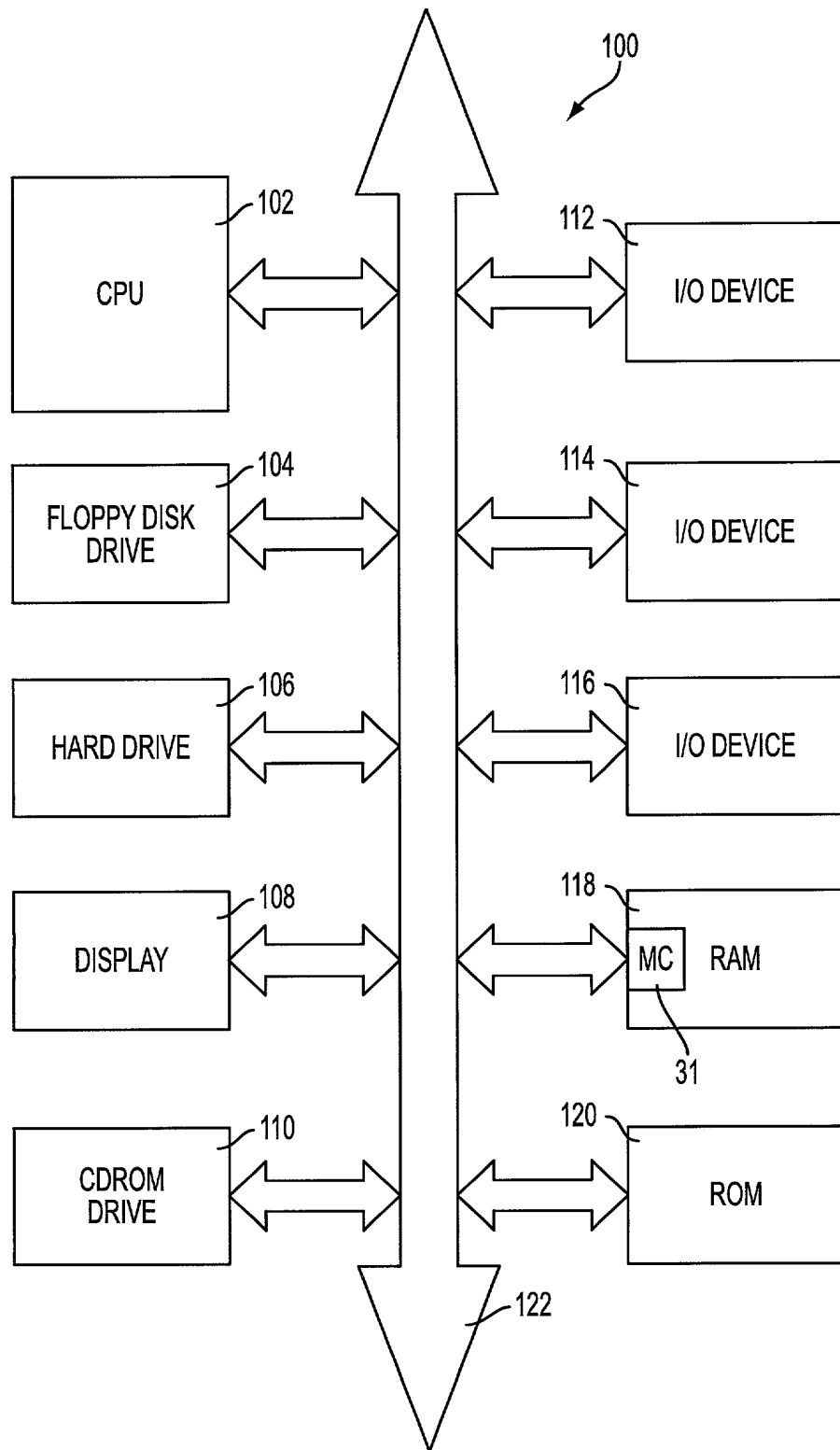


FIG. 10

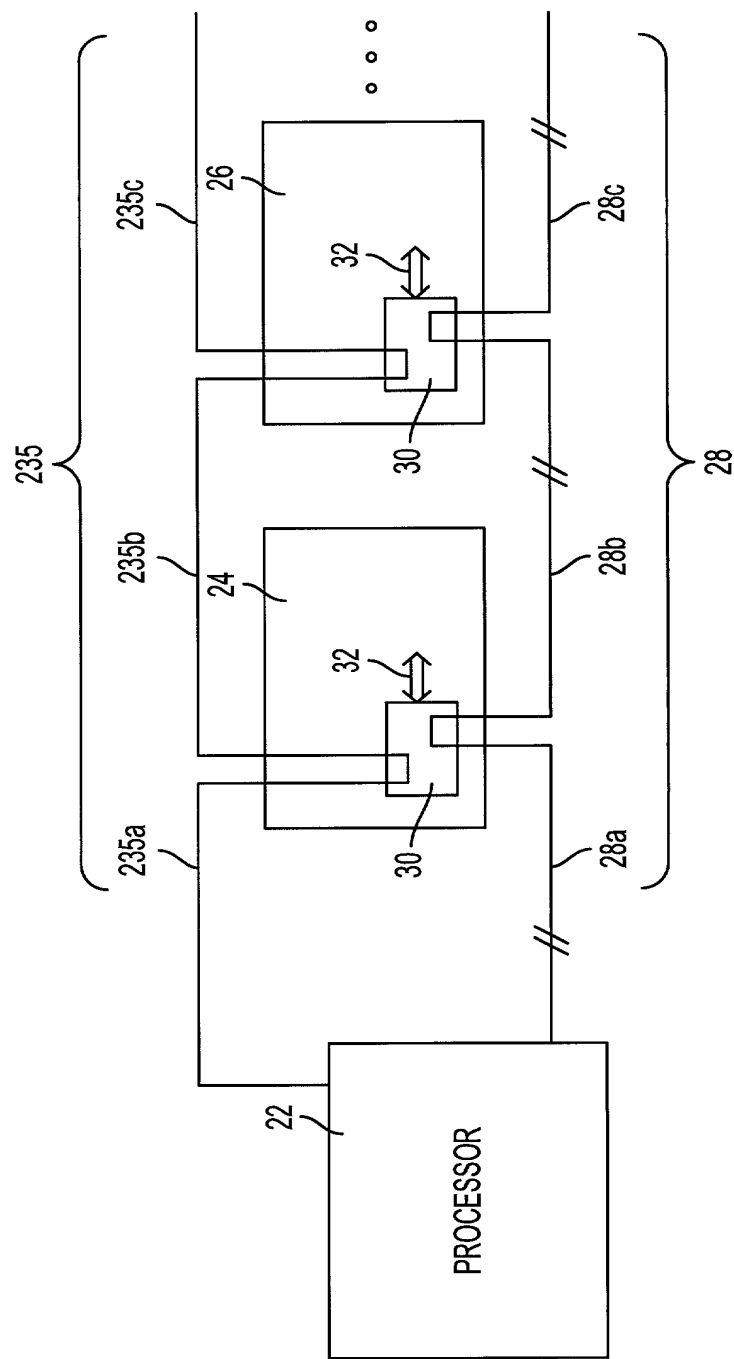


FIG. 11

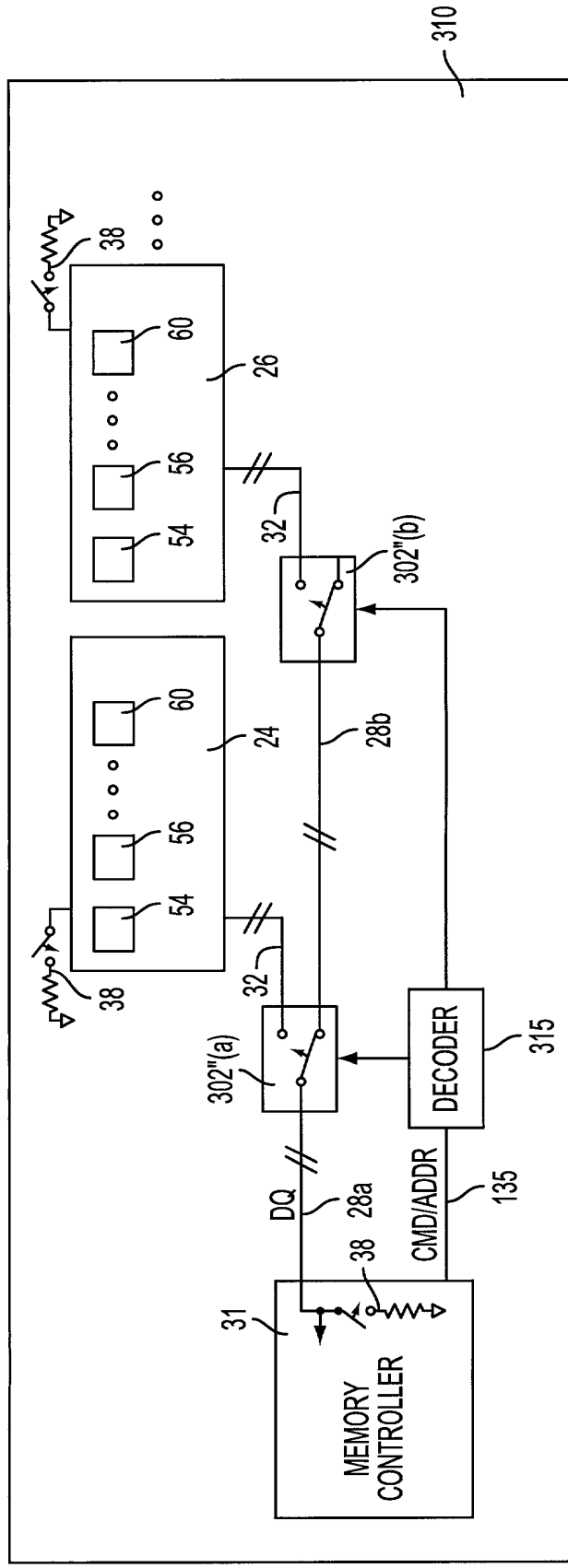
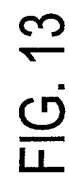


FIG. 12



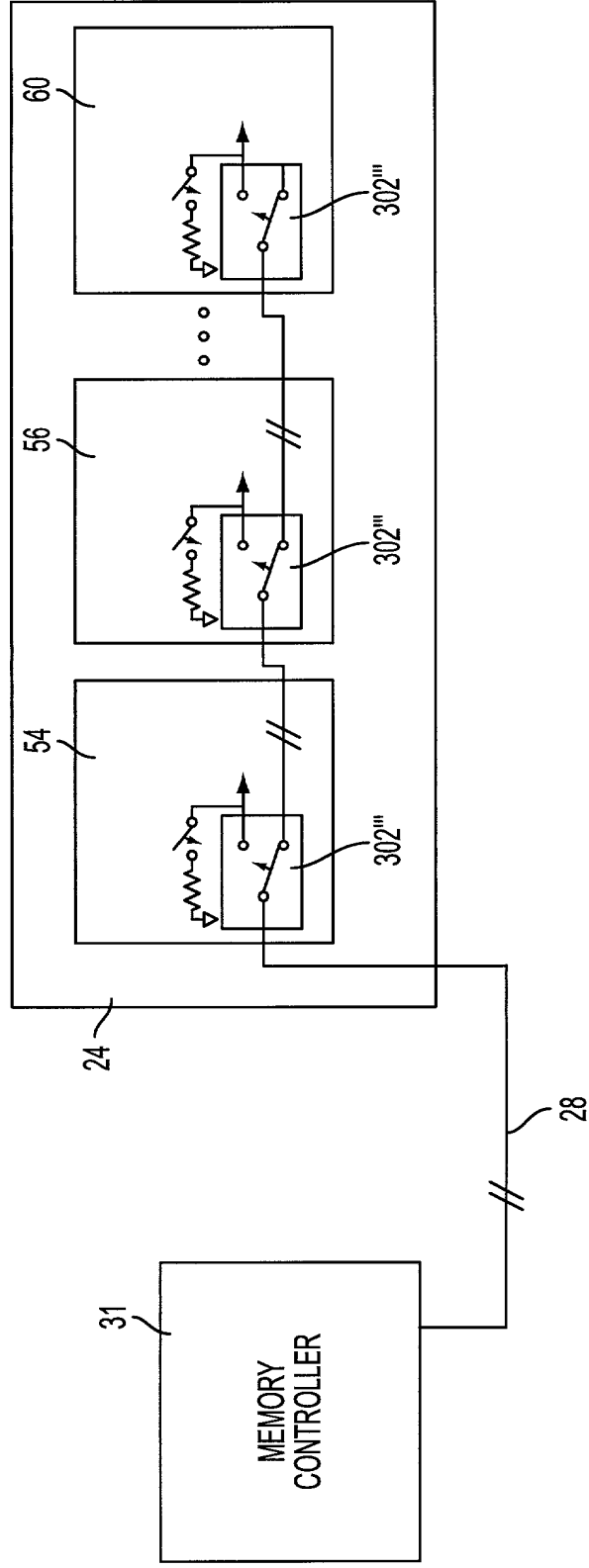


FIG. 14

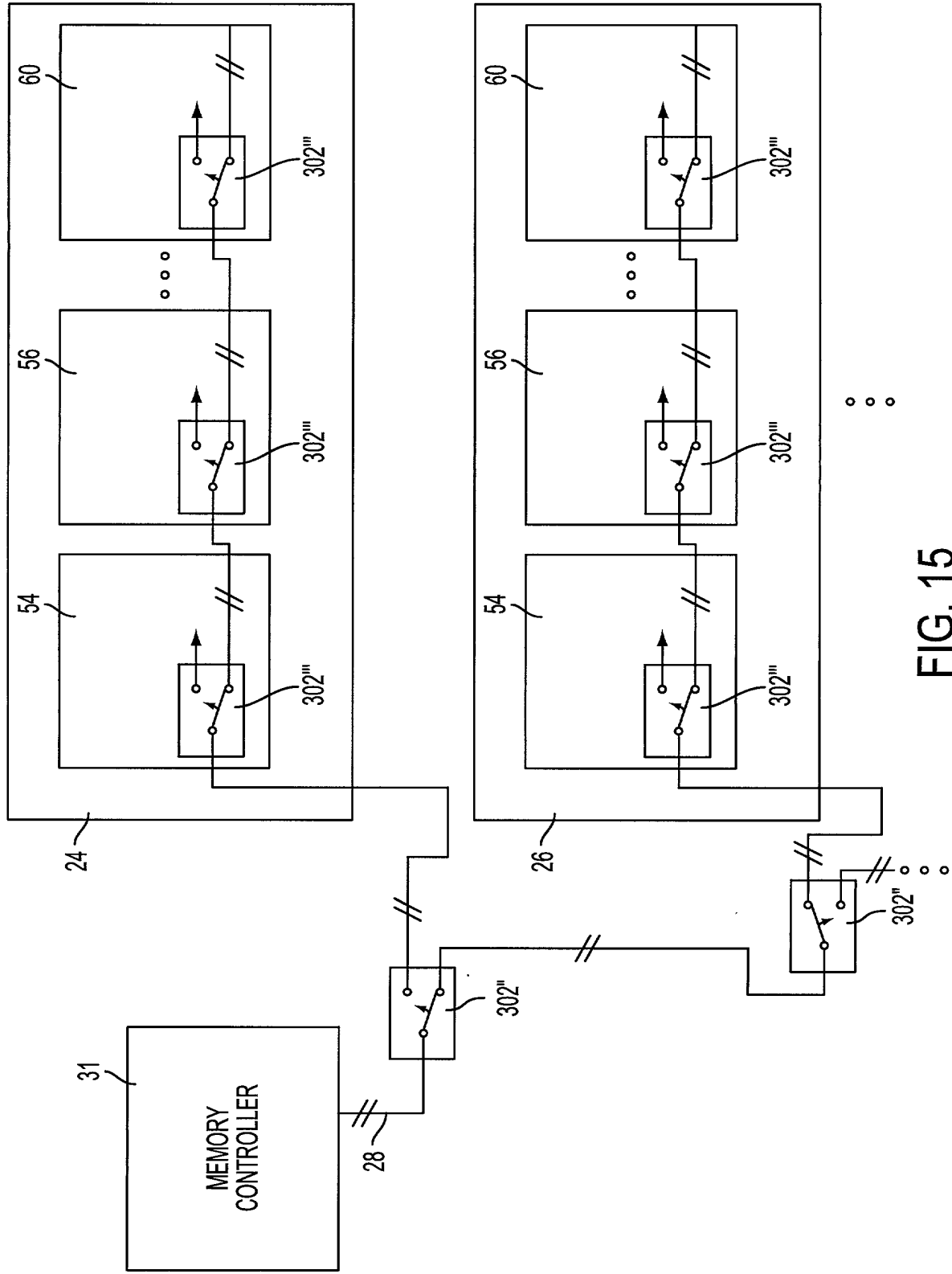


FIG. 15

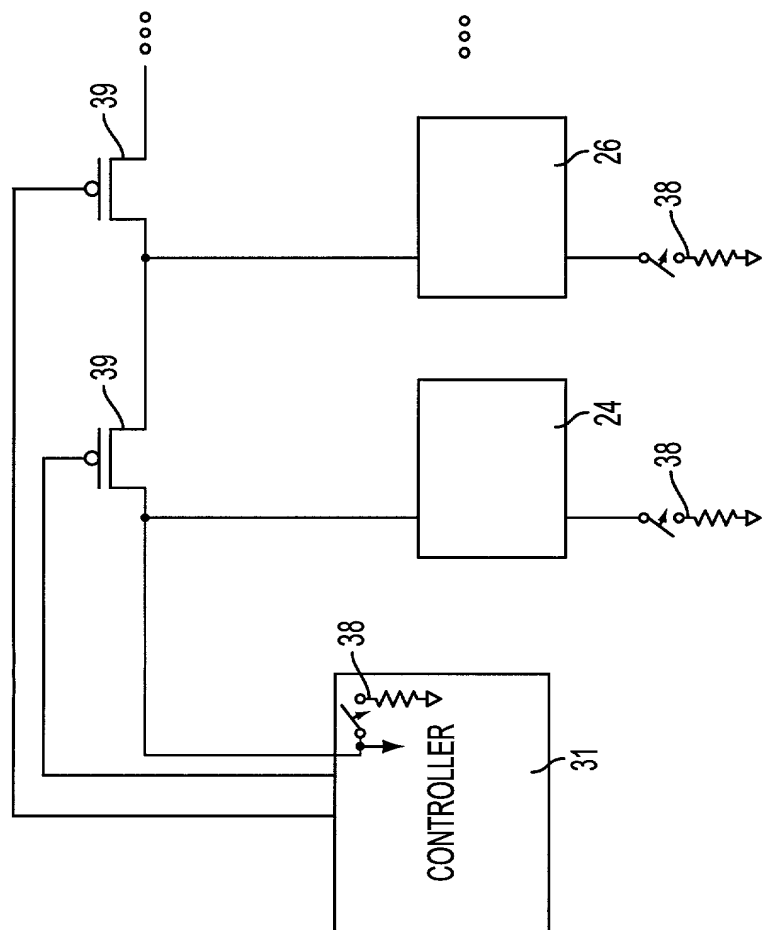


FIG. 16

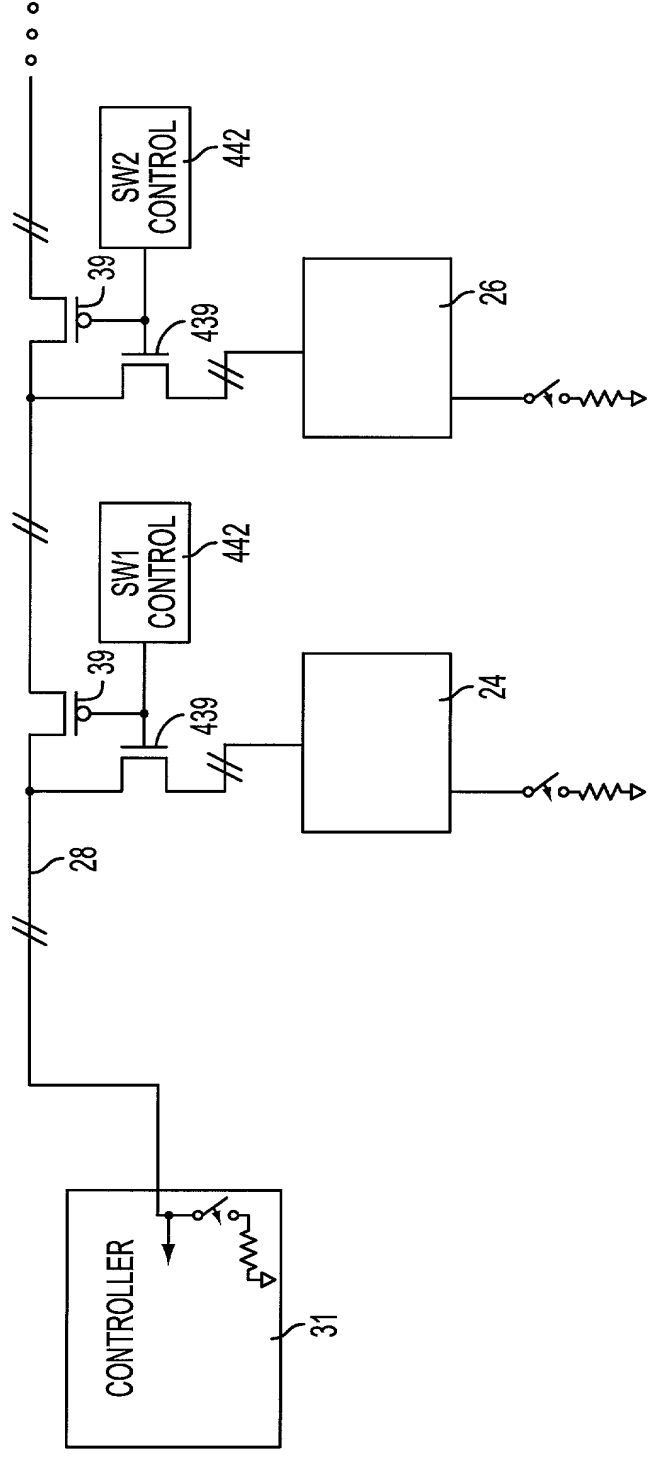


FIG. 17



FIG. 18 is a block diagram of a system 100 including a controller 31 and a plurality of programmable terminators 24, 26, 452. The controller 31 is connected to the programmable terminators 24, 26, 452 via a bus 28. Each programmable terminator 24, 26, 452 includes a switch 39. The switch 39 is controlled by the controller 31 via the bus 28. The switch 39 is used to connect the programmable terminator 24, 26, 452 to the bus 28. The bus 28 is connected to the controller 31 and the programmable terminators 24, 26, 452. The bus 28 is also connected to a power source 30. The power source 30 is connected to the bus 28 via a switch 39. The switch 39 is controlled by the controller 31 via the bus 28. The switch 39 is used to connect the power source 30 to the bus 28. The bus 28 is connected to the controller 31 and the programmable terminators 24, 26, 452. The bus 28 is also connected to a power source 30. The power source 30 is connected to the bus 28 via a switch 39. The switch 39 is controlled by the controller 31 via the bus 28. The switch 39 is used to connect the power source 30 to the bus 28.

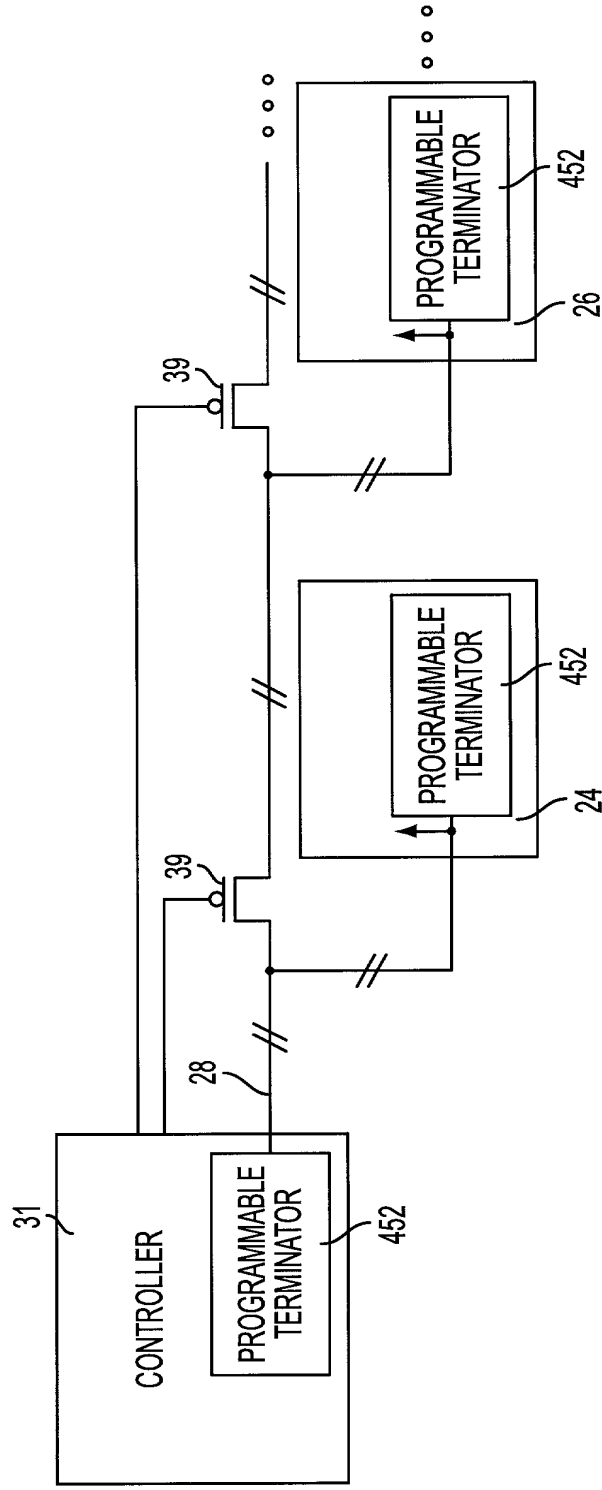


FIG. 18